

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: John M. Airey et al.

Application No.: 09/614,363

Group No.: 2628

Filed: July 12, 2000

Examiner: Jin-Cheng Wang

For: Display System Having Floating Point Rasterization and Floating Point Framebuffering

Attn: Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
WITHIN THREE MONTHS OF FILING OR  
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. § 1.97(b))

IDENTIFICATION OF TIME OF FILING THE ACCOMPANYING  
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. 37 C.F.R. § 1.97(b).

DATE: June 18, 2007



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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

NOTE: "An information disclosure statement shall be considered by the Office if filed by the applicant:  
 (1) Within three months of the filing date of a national application;  
 (2) Within three months of the date of entry of the national stage, as set forth in section 1.907 in an international application; or  
 (3) Before the strategic date of a first Office action on the merits, whichever event occurs last." 37 C.F.R. section 1.97(b).

NOTE: "Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to them, information that is material to patentability as defined in this section." 37 C.F.R. section 1.56(a).  
 "Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:  
 (1) such inventor named in the application;  
 (2) each attorney or agent who prepares or prosecutes the application; and  
 (3) every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee, or with anyone to whom there is an obligation to assign the application." 37 C.F.R. section 1.56(c).

NOTE: The "duty as described in section 1.56 will be met so long as the information in question was cited to the Office or submitted to the Office in the manner prescribed by sections 1.97(b), (d) and 1.75 before issuance of the patent." A.O. of January 9, 1992, 1135 O.G. 13-25 at 17.

WARNING: "No information disclosure statement may be filed in a provisional application." 37 C.F.R. section 1.51(d).

### List of Sections Forming Part of This Supplemental Information Disclosure Statement

The following sections are being submitted for this Supplemental Information Disclosure Statement:

(check sections forming a part of this statement; discard unused sections and number pages consecutively)

1. ☐ [X] Preliminary Statements
2. ☐ [X] Forms PTO/SB-08A and 08B (substitute for Form PTO-1449)
3. ☐ [ ] Statement as to Information Not Found in Patents or Publications
4. ☐ [ ] Identification of Prior Application in Which Listed Information Was Already Cited and for Which No Copies Are Submitted or Need Be Submitted
5. ☐ [ ] Cumulative Patents or Publications
6. ☒ [X] Copies of Listed Information Items Accompanying This Statement
7. ☐ [ ] Concise Explanation of Non-English Language Listed Information Items
  - 7A. ☐ [ ] EPO Search Report
  - 7B. ☐ [ ] English Language Version of EPO Search Report
8. ☐ [ ] Translation(s) of Non-English Language Documents
9. ☐ [ ] Concise Explanation of English Language Listed Information Items (Optional)
10. ☒ [X] Identification of Person(s) Making This Supplemental Information Disclosure Statement

(complete the following, if appropriate)

Sections \_\_\_\_\_, respectively, have been continued on ADDED PAGE(S).

NOTE: "Once the substantive requirements are met, the examiner has an obligation to consider the information." Notice of Appeal 20, 1992 (1139 O.G. 27-41, 37).

#### **Section I. Preliminary Statements**

Applicants submit herewith patents, publications or other information, of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

The filing of this Supplemental Information Disclosure Statement shall not be construed as a representation that a search has been made (37 C.F.R. section 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this Supplemental Information Disclosure Statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25 at 25.

**SECTION 2. FORMS PTO/SB/08A and 08B (formerly Form PTO-1449)**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: John M. Airey et al. Attorney Docket: 2839/115  
 Serial No: 09/614,363 Art Group Unit: 2628  
 Date Filed: July 12, 2000 Examiner Name: Jin-Cheng Wang  
 Invention: Display System Having Floating Point Rasterization and Floating Point Framebuffering

**LIST OF PATENTS AND PUBLICATIONS FOR  
 APPLICANT'S SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

| U.S. PATENT DOCUMENTS |                  |                 |               |           |                |
|-----------------------|------------------|-----------------|---------------|-----------|----------------|
| Examiner Initials     | Reference Number | Document Number | Issue Date    | Inventor  | Class/Subclass |
|                       | BI               | 6,292,185       | Sep. 18, 2001 | Ko et al. | 343/354        |

| OTHER DOCUMENTS   |                  |                    |  |
|-------------------|------------------|--------------------|--|
| Examiner Initials | Reference Number | Author             | Title of Article, Title of Journal, Volume Number, Page Numbers, Date  |
|                   | BI               | Mohr, Steven       | <i>The PixelFlow Texture and Image Subsystem</i> , University of North Carolina-Chapel Hill, April 1995, 11 pages.                                     |
|                   | BK               | Eyles, John et al. | <i>PixelFlow: The Realization</i> , 1997, 15 pages.  |
|                   | BL               | Olano, Marc et al. | <i>Procedural Primitives in a High Performance Hardware Accelerated, Z-Buffer Renderer</i> , University of North Carolina-Chapel Hill, 1997, 10 pages. |
|                   | BM               | Fuchs et al.       | <i>Supercomputing Power for Interactive Visualization, PixelFlow Progress Report</i> , University of North Carolina-Chapel Hill, April 1995, 62 pages. |
|                   | BN               | Prof. Alessandro   | <i>Parallel Processing for Image Computing</i> , Multiprocessors and Array Processors, 1988, pp. 91-93.  |
|                   | BO               | Borden, Bruce      | <i>Graphics Processing on a Graphics Supercomputer</i> , IEEE Computer Graphics & Applications, July 1989, pp. 56-62.                                  |
|                   | BP               | Ward, Greg         | <i>Real Pixels</i> , Graphic Gems II, Academic Press, Inc., 1991, pp. 80-83.   |

Applicants: John M. Airey et al.

Attorney Docket: 2839/115

Serial No: 09/614,363

Art Group Unit: 2628

Date Filed: July 12, 2000

Examiner Name: Jin-Cheng Wang

Invention: Display System Having Floating Point Rasterization and Floating Point Framebuffering

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

| OTHER DOCUMENTS   |                  |                     |   |
|-------------------|------------------|---------------------|---|
| Examiner Initials | Reference Number | Author              | Title of Article, Title of Journal, Volume Number, Page Numbers, Date   |
|                   | BQ               | Wilkes, Maurice     | <i>Arithmetic on the EDSAC</i> , IEEE Annals of the History of Computing, Vol. 19, No. 1, 1997, pp. 13-15   |
|                   | BR               | Wah, Chan Choong    | <i>Design of a Gate Array Matrix Processor</i> , IEEE Tencen 1993, Beijing, pp. 565-568   |
|                   | BS               |                     | <i>Mechanism to Clamp and Pack Lit Floating-Point Colors</i> , IBM Technical Disclosure Bulletin, Vol. 40, No. 3, March 1997, pp. 163-164   |
|                   | BT               | Akeley, Kurt et al. | <i>High-Performance Polygon Rendering</i> , Computer Graphics, Vol. 22, No. 4, August 1988, pp. 239-246   |
|                   | BU               | Lee, John A. N.     | <i>Howard Aiken's Third Machine: The Harvard Mark III Calculator or Aiken-Dahlgren Electronic Calculator</i> , IEEE Annals of the History of Computing, Vol. 22, No. 1, January-March 2000, pp. 62-81 |
|                   | BV               | Cross, Robert A.    | <i>Interactive Realism for Visualization Using Ray Tracing</i> , Proceedings of Visualization '95, October 29-November 3, 1995, IEEE Computer Society Press, pp. 19-26                                |
|                   | BW               | Rojas, Raul         | <i>Kenneth Zuse's Legacy: The Architecture of the Z3 and Z4</i> , IEEE Annals of the History of Computing, Vol. 19, No. 2, 1997, pp. 5-16   |
|                   | BX               |                     | <i>TMS34020, TMS34020 Graphics Processors (Data Sheet)</i> , Texas Instruments, 1993, pp. 1-81  |
|                   | BY               | Eyles, John et al.  | <i>FreeFlow™ Rasterizer Functional Description</i> , University of North Carolina at Chapel Hill, November 20, 1997, pp. 1-105  |

Applicant: John M. Airey et al. Attorney Docket: 2839/115  
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| OTHER DOCUMENTS      |                     |   |  |
|----------------------|---------------------|---|--|
| Examiner<br>Initials | Reference<br>Number | Author  | Title of Article, Title of Journal, Volume Number,<br>Page Numbers, Date   |
|                      | BZ                  | Burks, Arthur W. et al.   | <i>Preliminary Discussion of the Logical Design of an Electronic Computing Instrument</i> , Papers of John von Neumann on Computing and Computer Theory. The MIT Press, 1987, pp. 97-142 |
|                      | CA                  | Miranek, G.S.   | <i>Titan III Supercomputer Architectural Overview</i> , Computing 48, 1992, pp. 39-59  |
|                      | CB                  | Potmesil, Michael et al.  | <i>The Pixel Machine: A Parallel Image Computer</i> , Computer Graphics Vol. 23, No. 3, July 1989, pp. 69-78   |
|                      | CC                  | Diede, Tom et al.   | <i>The Titan Graphics Supercomputer Architecture</i> , Computer, September 1988, pp. 13-30   |
|                      | CD                  | William H. Manning, Cole M. Finver, Brian A. Mayer, ROBINS, KAPLAN, MILLER & CRESI L.L.P. | <i>Supplemental Answers to Plaintiff Silicon Graphics, Inc.'s First Set of Interrogatories to Defendant ATI Technologies, ULC (Nos. 2-3)</i> , January 18, 2007, 63 pages                |

Examiner Signature: \_\_\_\_\_

Date Considered: \_\_\_\_\_

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## Section 6. Copies of Listed Information Items Accompanying This Statement

*NOTE: 37 CFR, section 1.98(a)(2) requires that any information disclosure statement filed under section 1.92 shall include: "(A) legible copy of: (i) Each U.S. and foreign patent, (ii) Each publication or that portion which caused it to be listed, and (iii) All other information or that portion which caused it to be listed, except that no copy of a U.S. patent application need be included; ..."*

*NOTE: The wording in section 1.98(a)(2)(i) makes it clear that the requirement to submit a copy of each item of information listed in an information disclosure statement does not apply to the citation of a U.S. patent application. See also January 9, 1992, 1635 (35), 13 QJ, at 14.*

Legible copies of all items listed in Forms PTO/SB/08A and 08B (substitute for Form PTO-6449) accompany this information statement:

*(complete the following, if applicable)*

☐ [x] [Exception(s) to above:

**U.S. patent citations are not included pursuant to the United State Patent and Trademarks Office's September 21, 2004 waiver of the copy requirement in 37 CFR 1.98 for cited pending U.S. patent citations when the patent citations are available in the USPTO's IFW system.**

☐ [ ] Items in prior application, from which an earlier filing date is claimed for this application, as identified in Section 4.

☐ [ ] Cumulative patents or publications identified in Section 5



**Section 10. Identification of Person(s) Making This Supplemental Information Disclosure Statement**

The person making this certification is

*(check each applicable item)*

- (a) ☐ the inventor(s) who signs below

SIGNATURE OF INVENTOR

*(type name of inventor who is signing)*

- (b) ☐ an individual associated with the filing and prosecution of this application (37 C.F.R. section 1.56(c))

SIGNATURE OF INVENTOR

*(type name of inventor who is signing)*

- (c) ☒ the practitioner who signs below on the basis of the information:

*(check each applicable item)*

☐ supplied by the inventor(s).

☐ supplied by an individual associated with the filing and prosecution of this application. (37 C.F.R. section 1.56(c)).

☒ in the practitioner's file.



SIGNATURE OF PRACTITIONER

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*(type or print name of practitioner)*

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